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2-21-02
PATENT
3672-0121P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: THOMPSON, Michael O. et al. Conf.:
Appl. No.: NEW Group:
Filed: July 6, 2001 Examiner:
For: ADDRESSING OF MEMORY MATRIX

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, DC 20231

July 6, 2001

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE CLAIMS:

Please amend the claims as follows:

Sub 917
a' 8. (Amended) A method according to claim 7, characterized by the controlled voltage increment and voltage decrement being equal to each other.